From Theory to Practice: Detecting and Preserving Constant-Time

A story of constant time, struggles, and betrayals

Clémentine Maurice, CNRS, CRIStAL

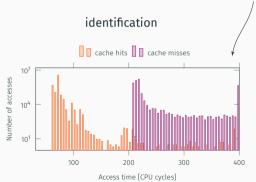
November 19, 2025 — C&ESAR Keynote

• hardware usually modeled as an abstract layer behaving correctly

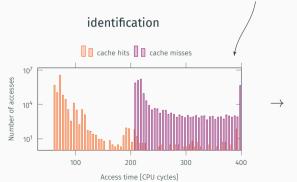
 hardware usually modeled as an abstract layer behaving correctly, but possible attacks

- hardware usually modeled as an abstract layer behaving correctly, but possible attacks
 - faults: bypassing software protections by causing hardware errors
 - side channels: observing side effects of hardware on computations

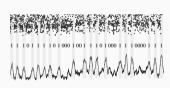
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- hardware usually modeled as an abstract layer behaving correctly, but possible attacks
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attack



- retrieving secret keys, keystroke timings
- bypassing OS security (ASLR)

Attacker model

Hardware-based attacks a.k.a physical attacks



Software-based attacks a.k.a micro-architectural attacks



VS





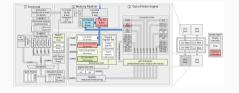
Physical access to hardware → embedded devices

Co-located or remote attacker \rightarrow complex systems

Micro-architectural side-channel attacks: Two faces of the same coin

Hardware







Implementation



Algorithm 1: Square-and-multiply exponentiation

Input: base b, exponent e, modulus n

Output: $b^e \mod n$

 $X \leftarrow 1$

for $i \leftarrow bitlen(e)$ downto 0 do

 $X \leftarrow \text{multiply}(X, X)$

if $e_i = 1$ then $X \leftarrow \text{multiply}(X, b)$

end

end

return X

Outline

- Part 1 Small example: Flush+Reload on GnuPG v 1.4.13
- Part 2 Constant-time: The Struggle
- Part 3 Constant-time: The Betrayal

Flush+Reload on GnuPG v 1.4.13

Part 1 Small example:

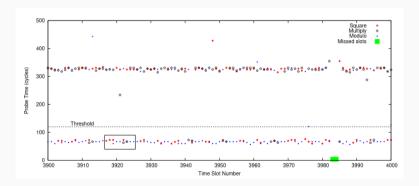
GnuPG 1.4.13 RSA square-and-multiply exponentiation

GnuPG version 1.4.13 (2013)

```
Algorithm 1: GnuPG 1.4.13 Square-and-multiply exponentiation
Input: base c, exponent d, modulus n
Output: c^d \mod n
X \leftarrow 1
for i \leftarrow bitlen(d) downto 0 do
    X \leftarrow \text{square}(X)
    X \leftarrow X \mod n
    if d_i = 1 then
        X \leftarrow \text{multiply}(X,c)
        X \leftarrow X \mod n
    end
end
return X
```

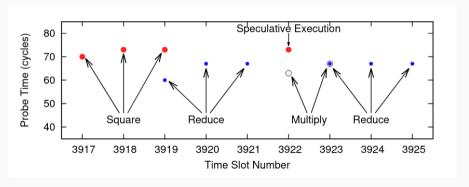
Attacking GnuPG 1.4.13 RSA exponentiation

 monitor the square and multiply functions with Flush+Reload to recover the bits of the secret exponent

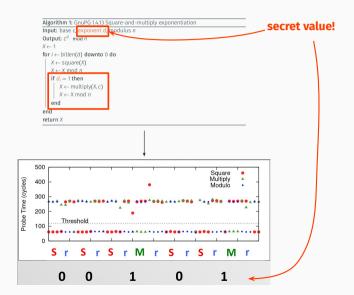


Attacking GnuPG 1.4.13 RSA exponentiation

 monitor the square and multiply functions with Flush+Reload to recover the bits of the secret exponent



Summary of the attack



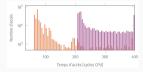
What just happened?

cache attack





exploits timing differences of memory accesses



attacker monitors lines accessed by the victim, not the content



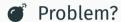
Part 2 Constant-time: The Struggle

Research Question: Which software implementation is vulnerable?

State of the art (more or less)

- 1. spend too much time reading OpenSSL code
- 2. find vulnerability
- 3. exploit it manually using known side channel \rightarrow e.g. CPU cache
- 4. publish
- 5. goto step 1





Side-channel vulnerability

Any branch or memory access that depends on a secret



♀ Solution!

Side-channel vulnerability

Any branch or memory access that depends on a secret



Constant-time programming

No branch or memory access
depends on a secret!



♀ Solution!

Side-channel vulnerability

Any branch or memory access that depends on a secret



Constant-time programming

No branch or memory access
depends on a secret!

That's easy, right?



That's easy, right?... right?

LadderLeak: Breaking ECDSA With Less Than One Bit Of Nonce Leakage Akira Takahashi

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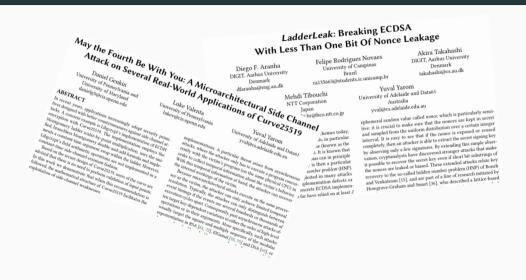
Although it is one of the most popular signature schemes today, ECDSA presents a number of implementation pitfalls, in particular due to the very sensitive nature of the random value (known as the nonce) generated as part of the signing algorithm. It is known that any small amount of nonce exposure or nonce bias can in principle lead to a full key recovery: the key recovery is then a particular instance of Boneh and Venkatesan's hidden number problem (HNP). That observation has been practically exploited in many attacks in the literature, taking advantage of implementation defects or side-channel vulnerabilities in various concrete ECDSA implementations. However, most of the attacks so far have relied on at least 2

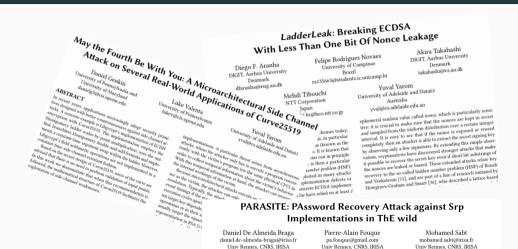
takahashi@cs.au.dk Yuval Yarom University of Adelaide and Data61 Australia yval@cs.adelaide.edu.au

ephemeral random value called nonce, which is particularly sensitive: it is crucial to make sure that the nonces are kept in secret and sampled from the uniform distribution over a certain integer interval. It is easy to see that if the nonce is exposed or reused completely, then an attacker is able to extract the secret signing key by observing only a few signatures. By extending this simple observation, cryptanalysts have discovered stronger attacks that make it possible to recover the secret key even if short bit substrings of the nonces are leaked or biased. These extended attacks relate key recovery to the so-called hidden number problem (HNP) of Boneh and Venkatesan [15], and are part of a line of research initiated by Howgrave-Graham and Smart [36], who described a lattice-based

DIGIT. Aarhus University

Denmark





Rennes, France

Protocols for password-based authenticated key exchange (PAKE)

allow two users sharing only a short, low-entropy password to

establish a secure session with a cryptographically strong key. The

dictionary attacks in which an attacker exhaustively enumerates

ABSTRACT

KEYWORDS

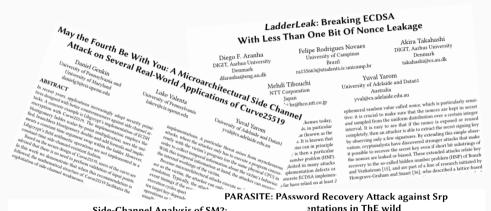
Rennes, France

SRP; PAKE; Flush+Reload; PDA; OpenSSL; micro-architectural attack

ACM Reference Format

ACM Reference Format: Daniel De Almeida Braga, Pierre-Alain Fouque, and Mohamed Sabt. 2021.

Rennes, France



Side-Channel Analysis of SM2: A Late-Stage Featurization Case Study

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numerates

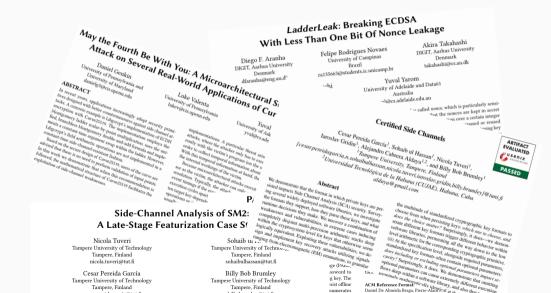
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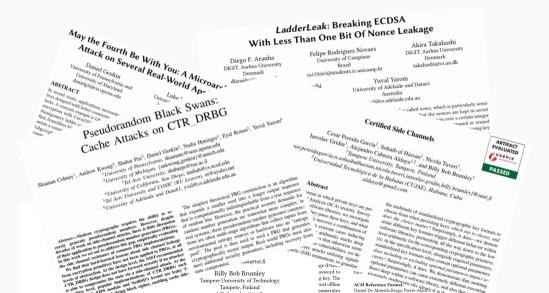
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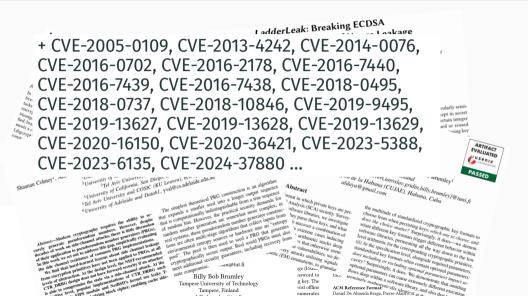
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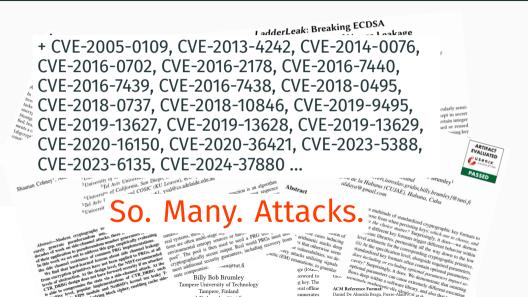
ige (PAKE) SRP: PAKE: Flush+Reload: PDA: OpenSSL: micro-architectural atssword to tack g key. The sist offline

Daniel De Almeida Braga, Pierre-Alain Fouque, and Mohamed Sabt. 2021

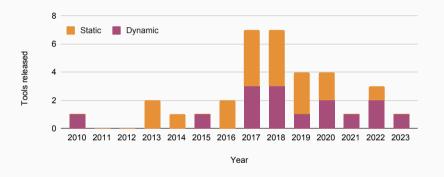






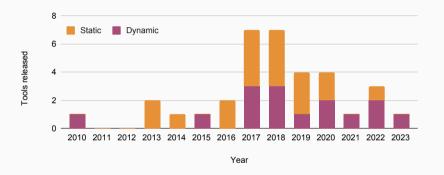


So many detection frameworks, yet so many attacks... Why?



Many tools published from 2017, 67% of tools are open source (23 over 34)

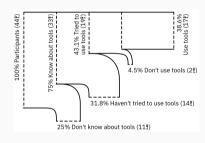
So many detection frameworks, yet so many attacks... Why?



Many tools published from 2017, 67% of tools are open source (23 over 34) Why are so many attacks still manually found?

Related Work

- do developers use CT tools? [S&P 2022]
 → most developers do not use them, or do not know about them
- how to improve the tool usability?
 [USENIX Sec 2024]
 → most developers find them really hard to use

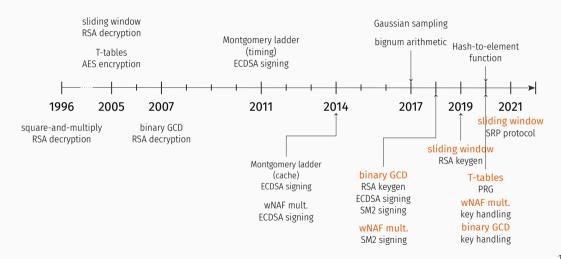


J. Jancar et al. ""They're not that hard to mitigate": What Cryptographic Library Developers Think About Timing Attacks". In: S&P. 2022.

M. Fourné et al. ""These results must be false": A usability evaluation of constant-time analysis tools". In: USENIX Security Symposium. 2024.

Would the tools actually work to automatically find recent vulnerabilities?

Comparing recent vulnerabilities (2017-2022) with past vulnerabilities



The SAME vulnerabilities keep resurfacing. Why? (1/2)

New contexts:

- Key generation [AsiaCCS 2018]
- Key parsing and handling [USENIX Sec 2020, S&P 2019]
- Random number generation [S&P 2020]

(Mostly OpenSSL) Vulnerable code stays in the library and the CT flag is not correctly set

The SAME vulnerabilities keep resurfacing. Why? (2/2)

New libraries

- MbedTLS sliding window RSA implementation [DIMVA 2017]
- Bleichenbacher-like attacks in MbedTLS, s2n, or NSS [S&P 2019]

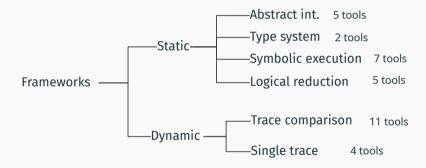
Vulnerability is found in OpenSSL but patches are not propagated to other libraries

Most vulnerabilities stem from code already known to be vulnerable

Side-channel vulnerability detection tools (1/2)

Ref	Year	Tool	Type	Methods	Scal.	Policy	Sound	Input	L	W	Е	В	Available
[85]	2010	ct-grind	Dynamic	Tainting	•	CT	0	Binary	/				_/
[15]	2013	Almeida et al.	Static	Deductive verification	0	CT	•	C source					
[55]	2013	CacheAudit	Static	Abstract interpretation	0	CO	•	Binary			/		✓
[22]	2014	VIRTUALCERT	Static	Type system	0	CT	•	C source			1		/
[70]	2015	Cache Templates	Dynamic	Statistical tests	0	CO	0	Binary	/				✓
[13]	2016	ct-verif	Static	Logical verification	0	CT	•	LLVM					1
[107]	2016	FlowTracker	Static	Type system	•	CT	•	LLVM	/				✓
[56]	2017	CacheAudit2	Static	Abstract interpretation	0	CT	•	Binary			/		
[28]	2017	Blazy et al.	Static	Abstract interpretation	•	CT	•	C source					
[17]	2017	Blazer	Static	Decomposition	•	CR	•	Java		/			
[48]	2017	Themis	Static	Logical verification	•	CR	•	Java	/	✓			
[127]	2017	CacheD	Dynamic	DSE	•	CO	0	Binary	/	/			
[136]	2017	STACCO	Dynamic	Trace diff	•	CR	0	Binary	/				✓
[106]	2017	dudect	Dynamic	Statistical tests	•	CC	0	Binary					✓
[117]	2018	CANAL	Static	SE	0	CO	•	LLVM		✓			✓
[47]	2018	CacheFix	Static	SE	•	CO	•	C	/	/			✓
[34]	2018	CoCo-Channel	Static	SE, tainting	•	CR	•	Java		✓			
[19]	2018	SideTrail	Static	Logical verification	0	CR	•	LLVM	/	/	/		1
[114]	2018	Shin et al.	Dynamic	Statistical tests	•	CO	0	Binary	/				
[132]	2018	DATA	Dynamic	Statistical tests	•	CT	0	Binary	/			1	✓
[133]	2018	MicroWalk	Dynamic	MIA	•	CT	0	Binary	/		✓		✓
[110]	2019	STAnalyzer	Static	Abstract interpretation	•	CT	•	С	/				1
[95]	2019	DifFuzz	Dynamic	Fuzzing	•	CR	0	Java		/			1
[126]	2019	CacheS	Static	Abstract interpretation, SE	•	CT	0	Binary	/	/			
[35]	2019	CaSym	Static	SE	•	CO	•	LLVM	✓	✓			
[54]	2020	Pitchfork	Static	SE, tainting	•	CT	0	LLVM	/	1			1
[66]	2020	ABSynthe	Dynamic	Genetic algorithm, RNN	•	CR	0	C source	✓				✓
[72]	2020	ct-fuzz	Dynamic	Fuzzing	•	CT	0	Binary	1	1			✓
[51]	2020	BINSEC/REL	Static	SE	•	CT	•	Binary	/	_/			1
[20]	2021	Abacus	Dynamic	DSE	•	CT	0	Binary	1		1		✓
[74]	2022	CaType	Dynamic	Type system	0	CO	•	Binary	✓			✓	
[134]	2022	MicroWalk-CI	Dynamic	MIA	•	CT	0	Binary, JS	/		/		✓
[140]	2022	ENCIDER	Static	SE	•	CT	•	LLVM	/	1			✓
[141]	2023	CacheQL	Dynamic	MIA, NN	•	CT	0	Binary	/		1	1	√ †

Side-channel vulnerability detection tools (2/2)



Benchmark: cryptographic operations

Unified benchmark representative of cryptographic operations:

- 5 tools: Binsec/Rel, Abacus, ctgrind, dudect, Microwalk-CI
- · 25 benchmarks from 3 libraries (OpenSSL, MbedTLS, BearSSL)
- · cryptographic primitives: symmetric, AEAD schemes, asymmetric

L. Daniel, S. Bardin, and T. Rezk. "Binsec/Rel: Efficient Relational Symbolic Execution for Constant-Time at Binary-Level". In: S&P. 2020.

Q. Bao et al. "Abacus: Precise Side-Channel Analysis". In: ICSE. 2021.

https://github.com/agl/ctgrind

O. Reparaz, J. Balasch, and I. Verbauwhede. "Dude, is my code constant time?" In: DATE. 2017.

J. Wichelmann et al. "Microwalk-CI: Practical Side-Channel Analysis for JavaScript Applications". In: CCS. 2022.

Benchmark results: cryptographic operations (selection)

	Binsec/Rel2	Abacus	ctgrind	Microwalk
	#V	#V	#V	#V
AES-CBC-bearssl (T)	36	36	36	36
AES-CBC-bearssl (BS)	0	0	0	0
AES-GCM-openssl (EVP)	0	0	70	8
RSA-bearssl (OAEP)	2 (🖺)	G	87	0
RSA-openssl (PKCS)	1 (🔀)	0	321	46
RSA-openssl (OAEP)	1 (🗷)	G *	546	61

- timeout limit (☒): 1 hour
- tools generally agree on symmetric crypto, but disagree on asymmetric crypto
- takeaway: support for vector instructions is essential

Benchmark: recent vulnerabilities

Replication of published vulnerabilities:

- 7 vulnerable functions from 3 publications
- both the function itself and its context are targeted
- · total: 11 additional benchmarks

Benchmark results: recent vulnerabilities (selection)

	Binsec/Rel2 Abacus		ctgrind		Mi	crowalk		
	V	T(s)	V	T(s)	V	T(s)	V	T(s)
RSA valid. (MbedTLS)		\blacksquare		490.01	√	0.40	√	278.94
GCD				37.74		0.21	√	22.96
modular inversion				242.10	√	0.24	√	141.82
RSA keygen (OpenSSL)		0.17	G	8.66		6.36	√	842.02
GCD	√				√	0.19	√	3.61
modular inversion					√	0.21	√	5.96

- some vulnerabilities are missed because of implicit flows
- most tools do not support tainting internal secrets

A Systematic Evaluation of Automated Tools for Side-Channel Vulnerabilities Detection in Cryptographic Libraries

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Lesly-Ann Daniel KU Leuven, imec-DistriNet Leuven, Belgium Mathéo Vergnolle Université Paris-Saclay, CEA, List Gif-sur-Yvettes, France

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> Clémentine Maurice Univ. Lille, CNRS, Inria Lille, France

Abstract

To protect cryptographic implementations from side-channel vulnerabilities, developers must adopt constant-time programming practices. As these can be error-prone, many side-channel detection tools have been proposed. Despite this, such vulnerabilities are still manually found in cryptographic libraries. While a recent paper by Jancar et al. shows that developers rarely perform side-channel detection, it is unclear if existing detection tools could have found these vulnerabilities in the first place.

To answer this question we surveyed the literature to build a classification of 34 side-channel detection frameworks. The classification we offer compares multiple criteria, including the methods used the scalability of the analysis or the threat model considered.

1 Introduction

Implementing cryptographic algorithms is an arduous task. Beyond functional correctness, the developers must also ensure that their code does not leak potentially secret information through side channels. Since Paul Kocher's seminal work [82], the research community has combed through software and hardware to find vectors allowing for side-channel attacks, from execution time to electromagnetic emissions. The unifying principle behind this class of attacks is that they do not exploit the algorithm specification but rather physical characteristics of its execution. Among the aforementioned attack vectors, the processor microarchitecture is of particular interest, as it is a shared resource between multiple programs. By observing the target execution through microarchitecture.

Part 3: Constant-time: The Betrayal

Constant-time code \neq constant-time binary (1/4)

- the compiler is not your friend, it just wants to make stuff fast
- recent example: Kyber implementation, CVE-2024-37880, June 03, 2024

Constant-time code \neq constant-time binary (2/4)

Expanding a string into an array of integers, the wrong way

```
void expand_insecure(int16_t r[256], uint8_t *msg){
   for(i=0; i<16; i++) { // outer loop: every byte of msg
       for(j=0; j<8; j++) { // inner loop: every bit in byte
          if ((msg[i] >> j) & 0x1) // branch on j-th msg bit
              r[8*i+i] = CONSTANT:
          else
              r[8*i+i] = 0:
```

Constant-time code \neq constant-time binary (3/4)

Expanding a string into an array of integers, the right way

Constant-time code \neq constant-time binary (4/4)

Now, what does the compiler do with your code?

```
expand insecure:
                   // x86 assembly
              eax, eax
.outer:
      xor
              ecx, ecx
.inner:
              r8d, byte ptr [rsi + rax]
      movzx
              edx. edx
      xor
              r8d, ecx // LSB test on (m[i] >> j)
      jae
              .skip
                        // unsafe branch
              edx, 1665 // load of CONSTANT (may be skipped)
.skip:
              word ptr [rdi + 2*rcx], dx
      mov
      inc
              rcx
              rcx. 8
      CMD
      ine
              .inner
                         // safe branch: inner loop
      inc
              rax
              rdi. 16
      add
              rax, 32
      cmp
      ine
               .outer
                         // safe branch: outer loop
      ret
```

Constant-time code \neq constant-time binary (4/4)

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       inc
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               rcx. 8
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                         // safe branch: inner loop
       inc
               rax
               rdi. 16
       add
               rax, 32
      cmp
      ine
               .outer
                         // safe branch: outer loop
       ret
```

```
expand_secure: // x86 assembly

[...]
.outer:

[...]
.inner:

movzx r8d, byte ptr [rsi + rax]

xor edx, edx

bt r8d, ecx

jae .skip // still here :(

edx, 1665

.skip:

ret
```

Constant-time code \neq constant-time binary (4/4)

Now, what does the compiler do with your code? Yes, it to optimizes it to

```
expand insecure:
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       mov
       inc
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               rcx. 8
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                         // safe branch: outer loop
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```

Corollary

- constant-time code can produce non-constant-time binary
- · any seemingly benign compiler update can break constant-time

L. Simon, D. Chisnall, and R. J. Anderson. "What You Get Is What You C: Controlling Side Effects in Mainstream C Compilers". In: EuroS&P. 2018.

M. Schneider et al. "Breaking Bad: How Compilers Break Constant-Time~Implementations". In: ASIA CCS. 2025.

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Known problem... but few studies:

- either limited to short snippets or older i386 programs
- or providing only quantitative insights

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Known problem... but few studies:

- · either limited to short snippets or older i386 programs
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- → lacking qualitative studies

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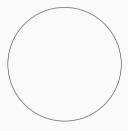
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How do compilers break CT guarantees?

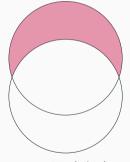
A two-fold problem:

binary CT violations



A two-fold problem:

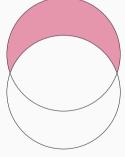
binary CT violations



source CT violations

A two-fold problem:

binary CT violations



source CT violations

Potential solution: only analyze verified libraries

- ightarrow risks limiting experiment's scope
- ightarrow developers often use non-verified libraries

A two-fold problem:

binary CT violations



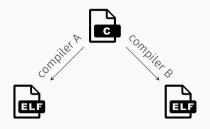
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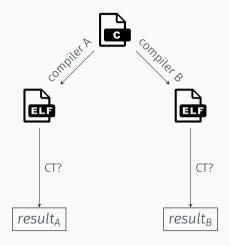
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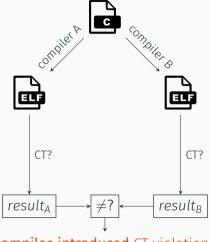
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...or apply manual filtering?

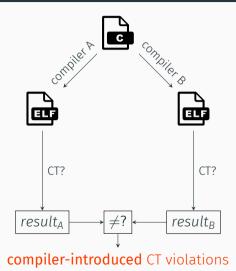
- \rightarrow done in Schneider et al.
- ightarrow risks missing leakages
- ightarrow thwarted by function inlining







compiler-introduced CT violations



Source benchmarks

MbedTLS and BearSSL from previous works

Compilers

LLVM 12/18 and GCC 9/13, O3 and Os

CT detection

Dynamic approach: Microwalk

Results

	LLV	M 03	GCC O3		
Binaries	v12	v18	v9	v13	
RSA-mbedtls (PKCS)	47	47	52	48 ▼	
RSA-mbedtls (OAEP)	46	48 🔺	49	49	
ECDSA-mbedtls	60	64 ▲	61	62 ▲	
RSA-bearssl (OAEP)	0	1 🔺	0	0	
ECDSA-bearssl	0	1 🔺	0	0	
poly_frommsg	0	1 🔺	0	0	
jump_threading	0	0	1	1	
loop_unswitching	1	1	1	1	
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- → LLVM: general increase in newer versions
- $\rightarrow\,$ not so much for GCC
- ightarrow both compilers can break CT

Pass analysis (1)

We analyzed the detected CT violations using Compiler Explorer:

- → OptPipeline tool allows us to isolate problematic passes
- ightarrow GCC and LLVM break CT in different ways: code patterns and optimizations
- → Limitation: manual analysis

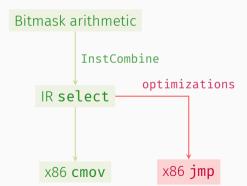
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in LLVM:



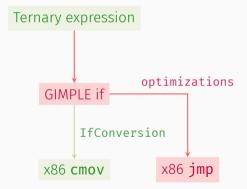
Pass analysis (2)

Different pathways to breaking CT...

in LLVM:

Bitmask arithmetic **InstCombine** optimizations IR select x86 **jmp** x86 cmov

in GCC:



Example: RSA-bearssl in LLVM (1)

Goal: perform a CT array access for windowed RSA modular exponentiation

```
for (int u = 1; u < N; u++) {
 uint32 t m;
 m = -EQ(u. secret):
 for (int v = 1: v < M: v++) {
   t2[v] |= m & base[v];
 base += M:
C source
```

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for (int u = 1; u < N; u++) {
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                                            uint32 t m;
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                             Inlining
                                           m = (u == secret);
 m = -EQ(u. secret):
 for (int v = 1: v < M: v++) {
                                            for (int v = 1; v < M; v++) {
                                              t2[v] \mid = select(m, base[v], 0);
   t2[v] |= m & base[v];
                               InstCombine
                                            base += M:
 base += M:
                                          LLVM IR (represented as C for clarity)
C source
```

Example: RSA-bearssl in LLVM (2)

This transformation by itself is safe...

```
for (int u = 1; u < N; u++) {
   uint32_t m;

m = (u == secret);
   for (int v = 1; v < M; v++) {
     t2[v] |= select(m, base[v], 0);
   }
   base += M;
}</pre>
```

Example: RSA-bearssl in LLVM (2)

This transformation by itself is safe... but allows further unsafe optimizations!

```
for (int u = 1; u < N; u++) {
                                             for (int u = 1: u < k: u++) {
 uint32 t m:
                                               uint32 t m:
 m = (u == secret):
                                               m = (u == secret):
  for (int v = 1; v < M; v++) {
                                               if (m) {
   t2[v] |= select(m, base[v], 0);
                                                 for (int v = 1; v < M; v++) {
                                                   t2[v] |= base[v];
 base += M:
                                               base += M;
```

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for (int u = 1: u < N: u++) {
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 m = (u == secret):
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  for (int v = 1; v < M; v++) {
                                               for (int v = 1; v < M; v++) {
   t2[v] \mid = select(m, base[v], 0);
                                              \rightarrow if (m) {
                                    CmovConversion t2[v] |= base[v];
 base += M:
                                                base += M;
```

What can we do about it?



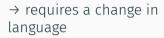
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→ requires a change in language

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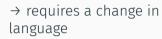
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→ requires a change in compiler

What can we do about it?



high-assurance cryptography (e.g., Jasmin)





specialized compilers (e.g., CompCert)

→ requires a change in compiler



code modifications and assembly

→ can break with compiler updates, portability issues

Mitigations

We investigate a simple mitigation: disabling problematic optimizations

- → using (sometimes undocumented) compiler flags
- ightarrow GCC: we disable loop unswitching, jump threading and path splitting
- ightarrow LLVM: we disable loop unswitching, loop vectorization and cmov conversion

Mitigations

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- ightarrow LLVM: we disable loop unswitching, loop vectorization and cmov conversion

Evaluation

- ightarrow effectiveness: rerun our benchmarks compiled with the mitigating flags
- → performance: reusing the libraries' existing performance benchmarks

Results

	LLVM O3		GCC O3	
Mitig.? Binaries	No	Yes	No	Yes
RSA-mbedtls (PKCS)	47	46 ▼	48	50 ▲
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RSA-bearssl (OAEP)	1	0 🔻	0	0
ECDSA-bearssl	1	0 ▼	0	0
poly_frommsg	1	0 ▼	0	0
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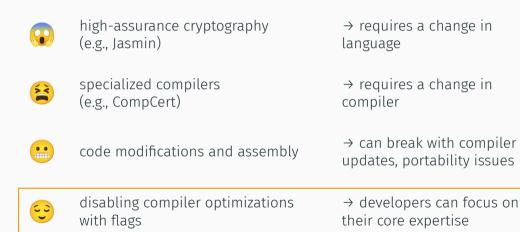
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- · Decrease in vulnerability
- · CT binaries remain CT
- Negligible performance impact
 - \rightarrow BearSSL: -3.30% (GCC), -0.43% (LLVM)
 - \rightarrow MbedTLS: -0.71% (GCC), -1.14% (LLVM)

What can we do about it? (cont'd)



Fun with flags: How Compilers Break and Fix Constant-Time Code

Antoine Geimer Univ. Lille, CNRS, Inria Univ. Rennes, CNRS, IRISA antoine.geimer@inria.fr Clémentine Maurice Univ. Lille, CNRS, Inria clementine.maurice@inria.fr

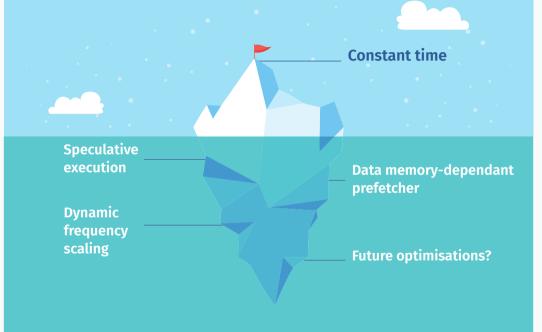
Abstract—Developers rely on constant-time programming to prevent timing side-channel attacks. But these efforts can be undone by compilers, whose optimizations may silently reintroduce leaks. While recent works have measured the extent of such leakage, they leave developers without actionable insights: which optimization passes are responsible, and how to disable them without modifying the compiler remains unclear.

In this paper, we conduct a qualitative analysis of how compiler optimizations break constant-time code. We construct a dataset of compiler-introduced constant-time violations and analyze the internals of two widely used compilers, GCC and LLVM, to identify the specific optimization passes responsible. Our key

can re-implement critical functions in assembly snippets for each targeted architecture – a time-consuming task that risk introducing more bugs. On the other hand they can purposefully complexify their code to counter the compiler's optimizations – hardly a resilient approach as compilers improve.

Problem. While a mix of both approaches is generally applied in cryptographic libraries, compiler-introduced side-channel vulnerabilities are still regularly found [8], [41]. In fact, recent studies showed that such vulnerabilities might be much more common than previously thought [42], [27]. Newer

Perspectives & Conclusion



Code that is "constant-time"

(and considered secure until recently)

can be vulnerable too!

Conclusions

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Conclusions

- first paper by Kocher in 1996: almost 30 years of research in this area
- · domain still in expansion: increasing number of papers published since 2015
- · micro-architectural attacks & defenses require a:
 - \cdot low-level understanding of hardware \rightarrow micro-architecture, reverse-engineering
 - low-level understanding of software \rightarrow program analysis, compilation, cryptography...
- \rightarrow work across all abstraction layers!

Thank you!

Contact

✓ clementine.maurice@inria.fr

From Theory to Practice: Detecting and Preserving Constant-Time

A story of constant time, struggles, and betrayals

Clémentine Maurice, CNRS, CRIStAL

November 19, 2025 — C&ESAR Keynote

Recommendations

#1 Support for vector instructions

#2 Support for indirect flows

#3 Support for internally generated secrets (e.g. key generation)

#4 Promote usage of a standardized benchmark

#5 Improve usability for static tools (e.g. core-dump initialization)

#6 Make libraries more static analysis friendly